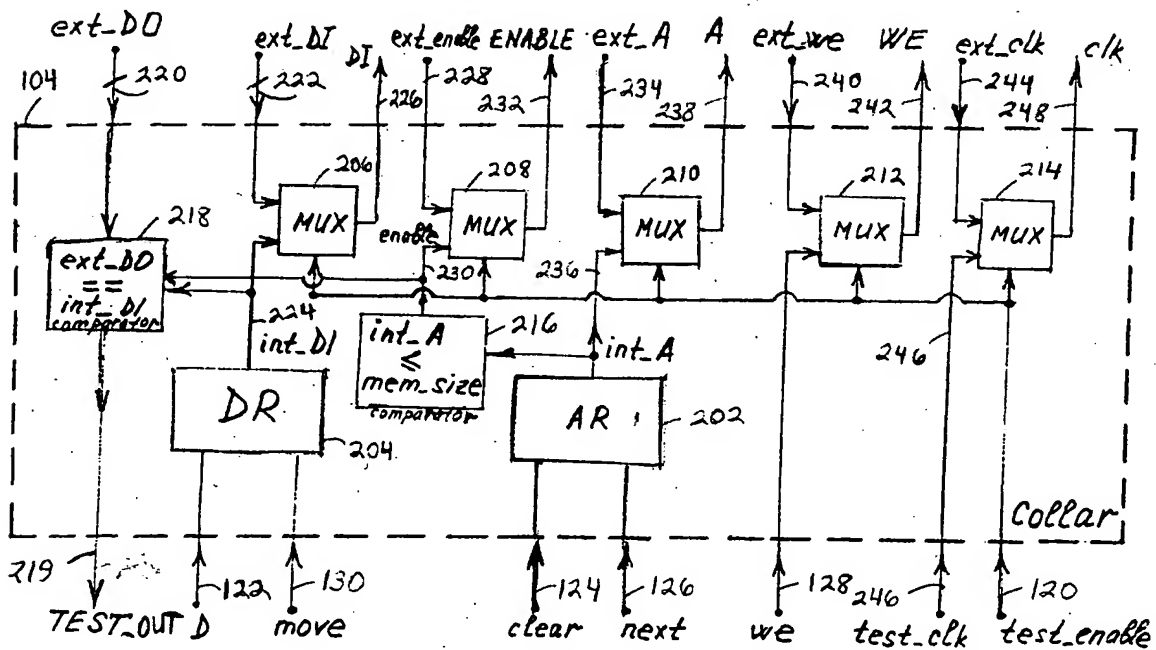
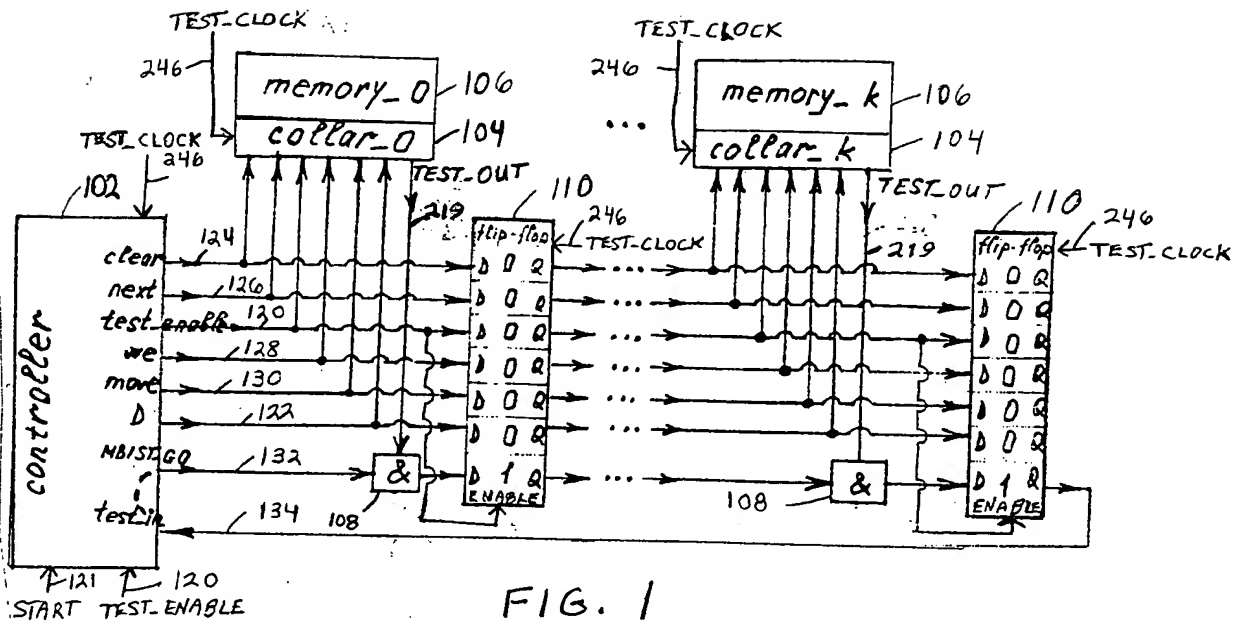


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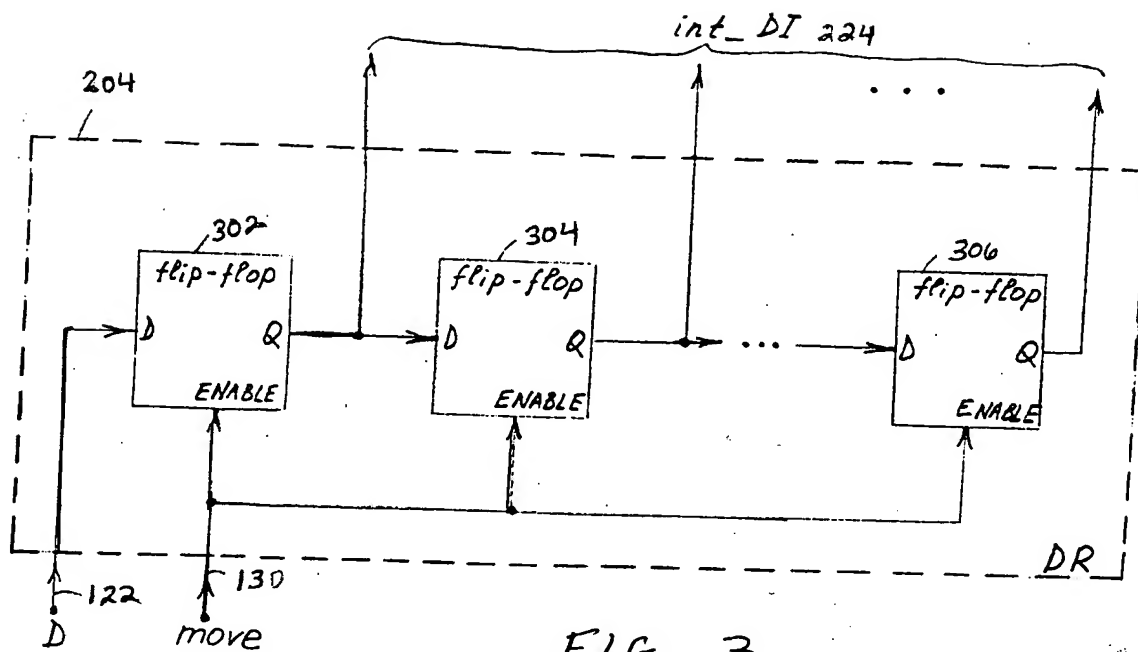


FIG. 3

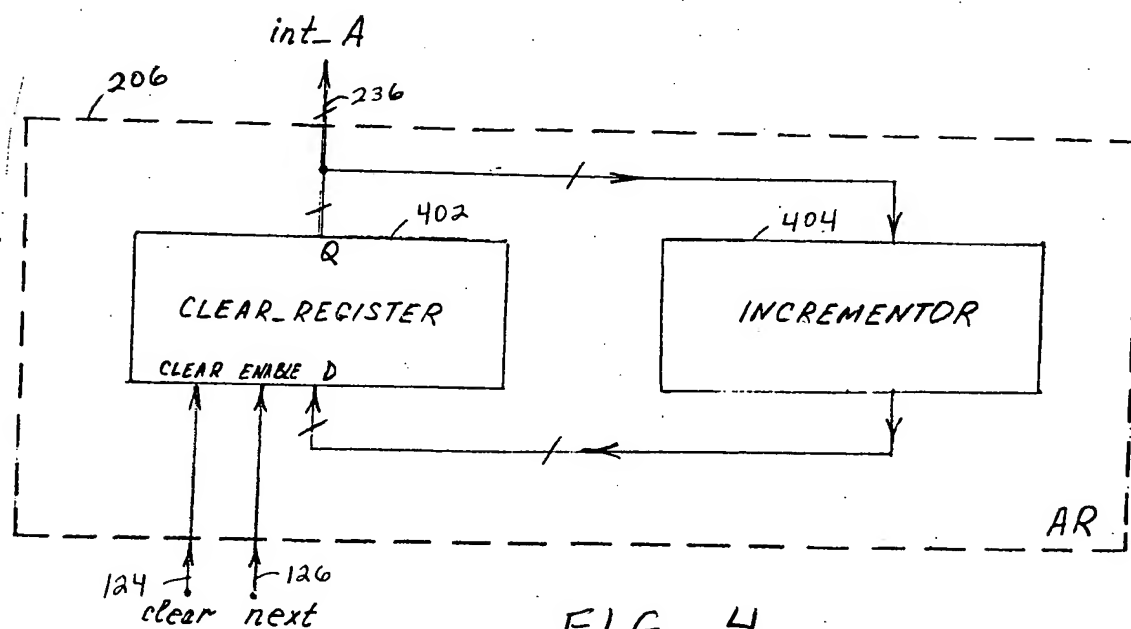


FIG. 4

500

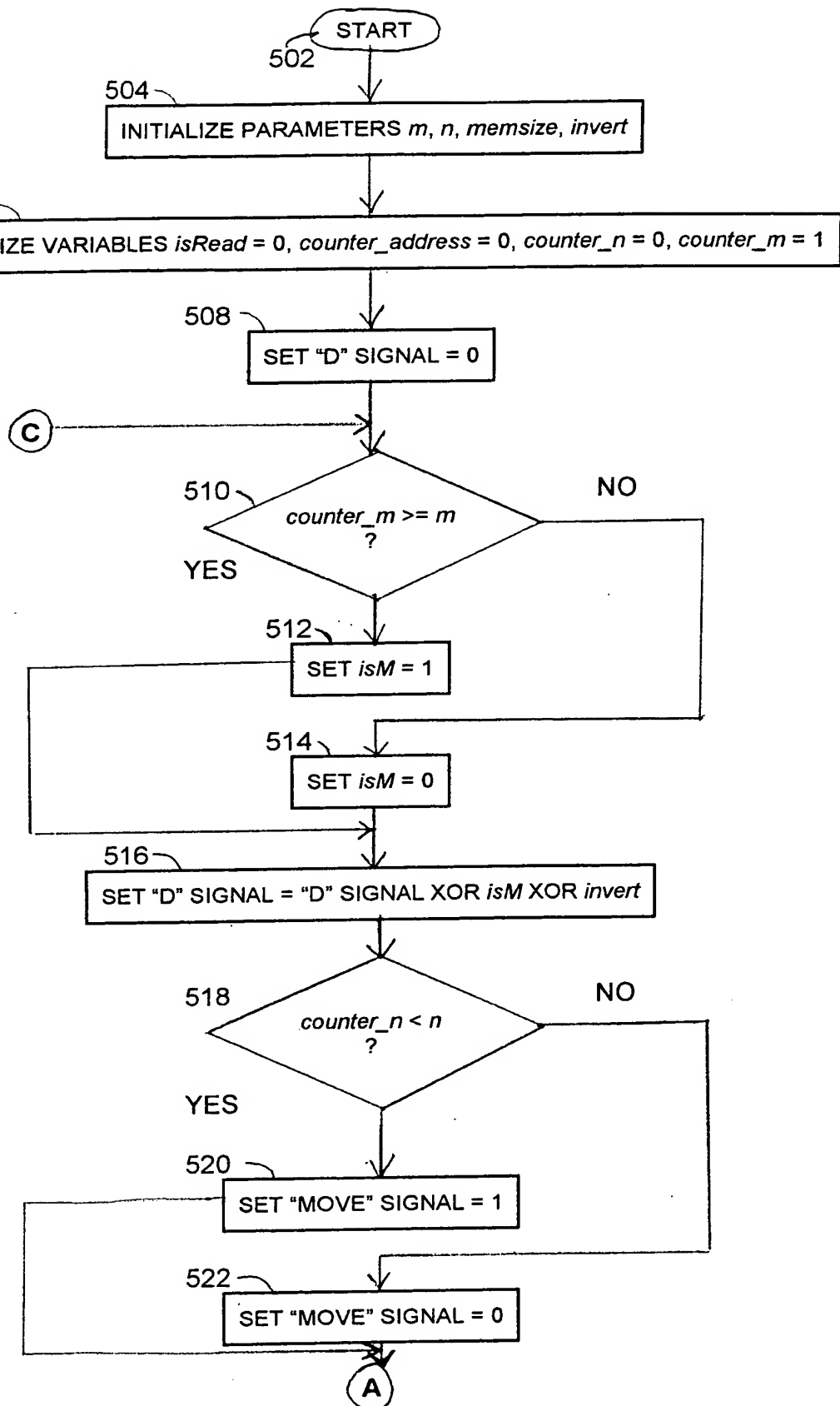


FIG. 5A

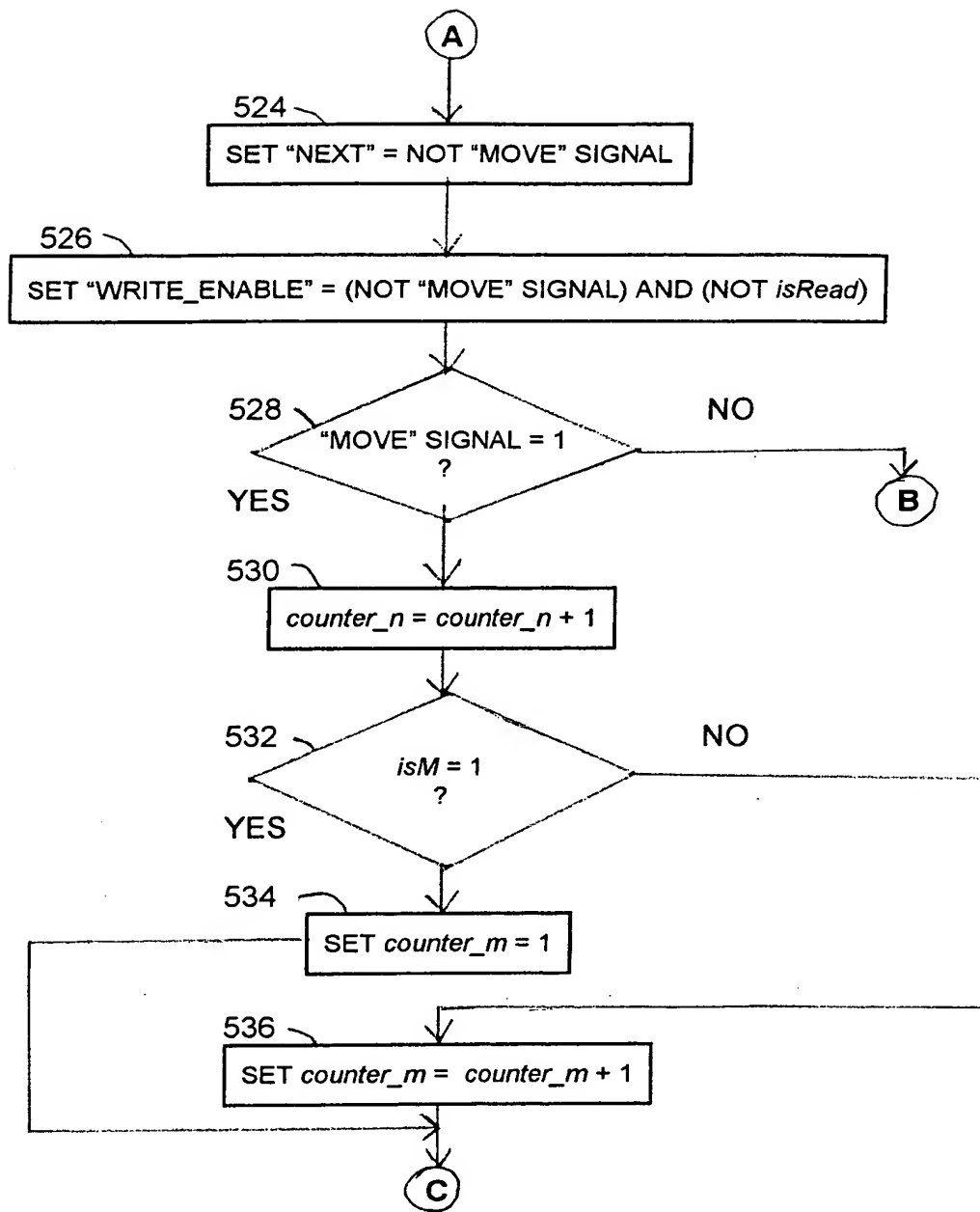


FIG. 5B

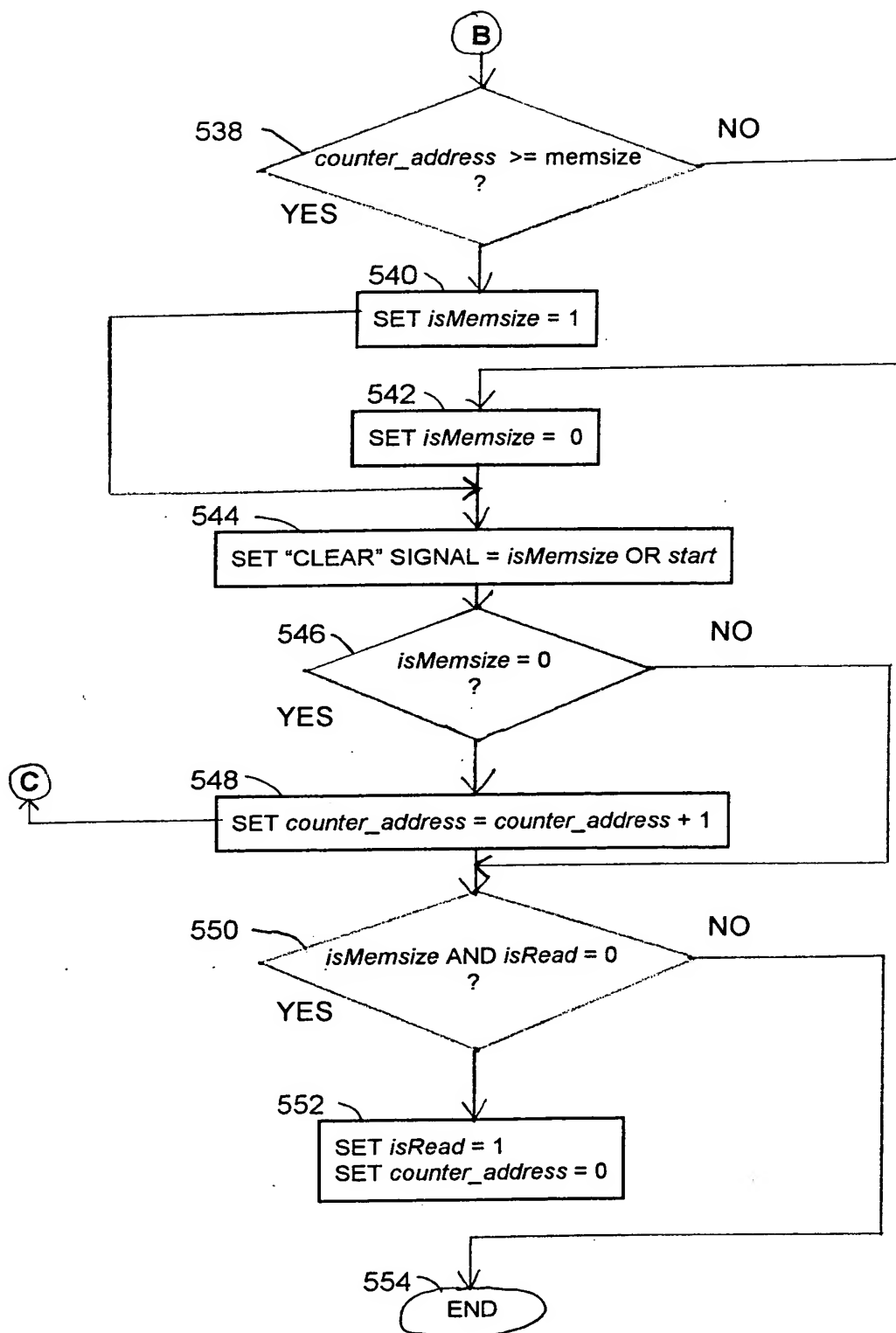


FIG. 5C

	602		604		606	
	fill DR (n steps)		memory write (memsize steps)		memory read (memsize steps)	
clear	1 0 0 ...	0	0 0 ...	0	1 0 ...	0
move	1 1 1 ...	1	0 0 ...	0	0 0 ...	0
next	0 0 0 ...	0	1 1 ...	1	1 1 ...	1
we	0 0 0 ...	0	1 1 ...	1	0 0 ...	0
D	$\underbrace{0 \dots 0}_{m} \underbrace{1 \dots 1}_{m} \underbrace{0 \dots 0}_{m} \dots$	$\alpha \oplus \text{isM} \dots$	$\alpha \oplus \text{isM} \dots$	$\alpha \oplus \text{isM} \dots$	$\alpha \oplus \text{isM} \dots$	$\alpha \oplus \text{isM} \dots$

FIG. 6

	802		804		806		808	
	fill DR (n steps)		memory write $\text{memsize} + \lfloor \frac{\text{memsize}}{K} \rfloor$ steps		fill DR (n steps)		memory read $\text{memsize} + \lfloor \frac{\text{memsize}}{K} \rfloor$	
clear	1 0 0 0 ...	0	0 ...	0	1 0 0 ...	0	0 ...	0
move	1 1 1 1 ...	1	0 ... 0 1 0 ... 0 1 ...		1 ... 1	0 ... 0 1 0 ... 0 1 ...		
next	0 0 0 0 ...	0	1 ... 1 0 1 ... 1 0 ...		0 ... 0	1 ... 1 0 1 ... 1 0 ...		
we	0 0 0 0 ...	0	1 ... 1 0 1 ... 1 0 ...		0 ... 0	0 ...		0
D	$\underbrace{0 1 0 1 \dots}_{n} \underbrace{\bar{\alpha} \alpha}_{k} \underbrace{\alpha \dots \alpha}_{k} \underbrace{\bar{\alpha} \bar{\alpha} \dots \bar{\alpha}}_{k} \alpha \dots$		$\underbrace{0 1 0 1 \dots}_{n} \underbrace{\bar{\alpha} \alpha}_{k} \underbrace{\alpha \dots \alpha}_{k} \underbrace{\bar{\alpha} \bar{\alpha} \dots \bar{\alpha}}_{k} \alpha \dots$		$\underbrace{0 1 0 1 \dots}_{n} \underbrace{\bar{\alpha} \alpha}_{k} \underbrace{\alpha \dots \alpha}_{k} \underbrace{\bar{\alpha} \bar{\alpha} \dots \bar{\alpha}}_{k} \alpha \dots$			

FIG. 8

700

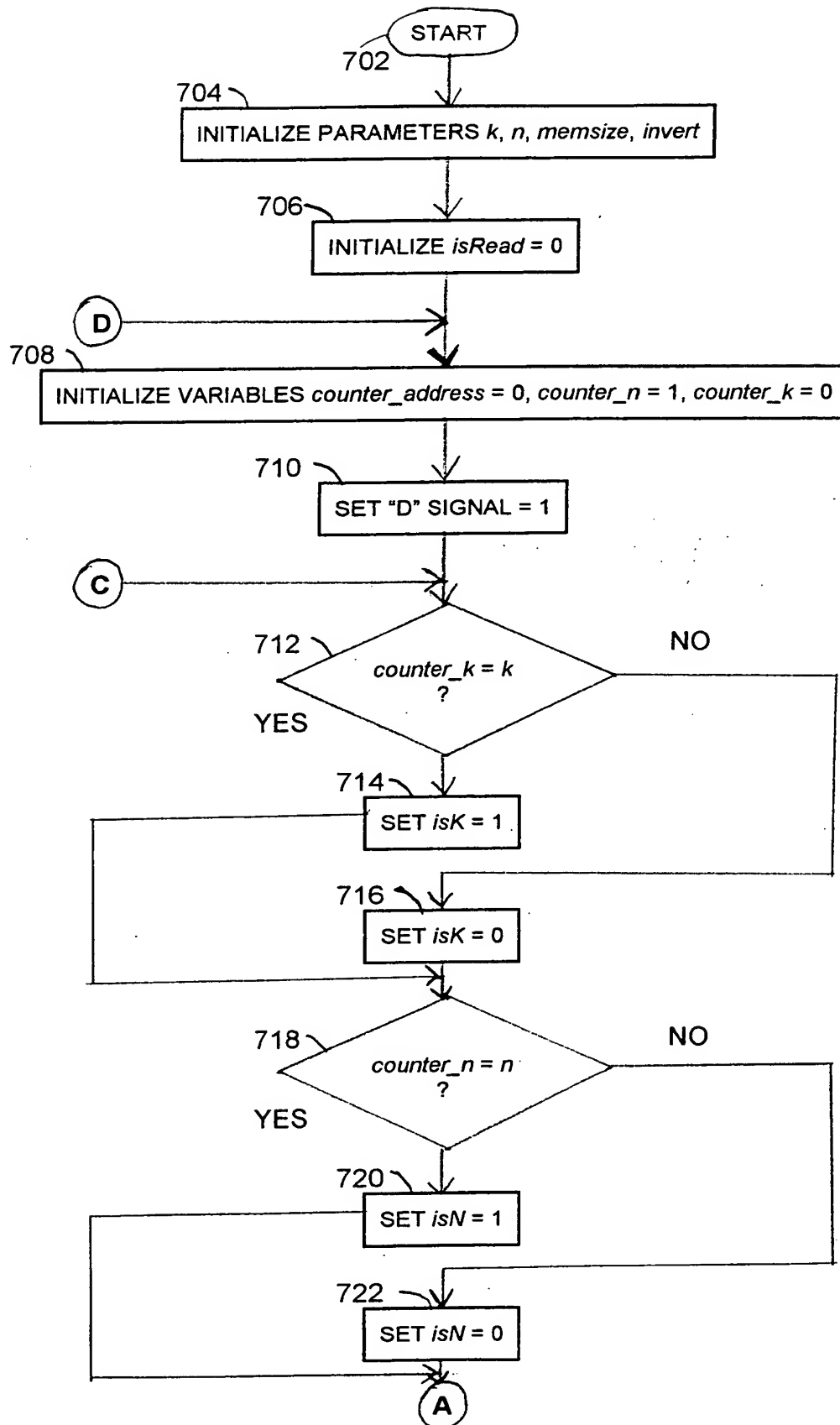


FIG. 7A

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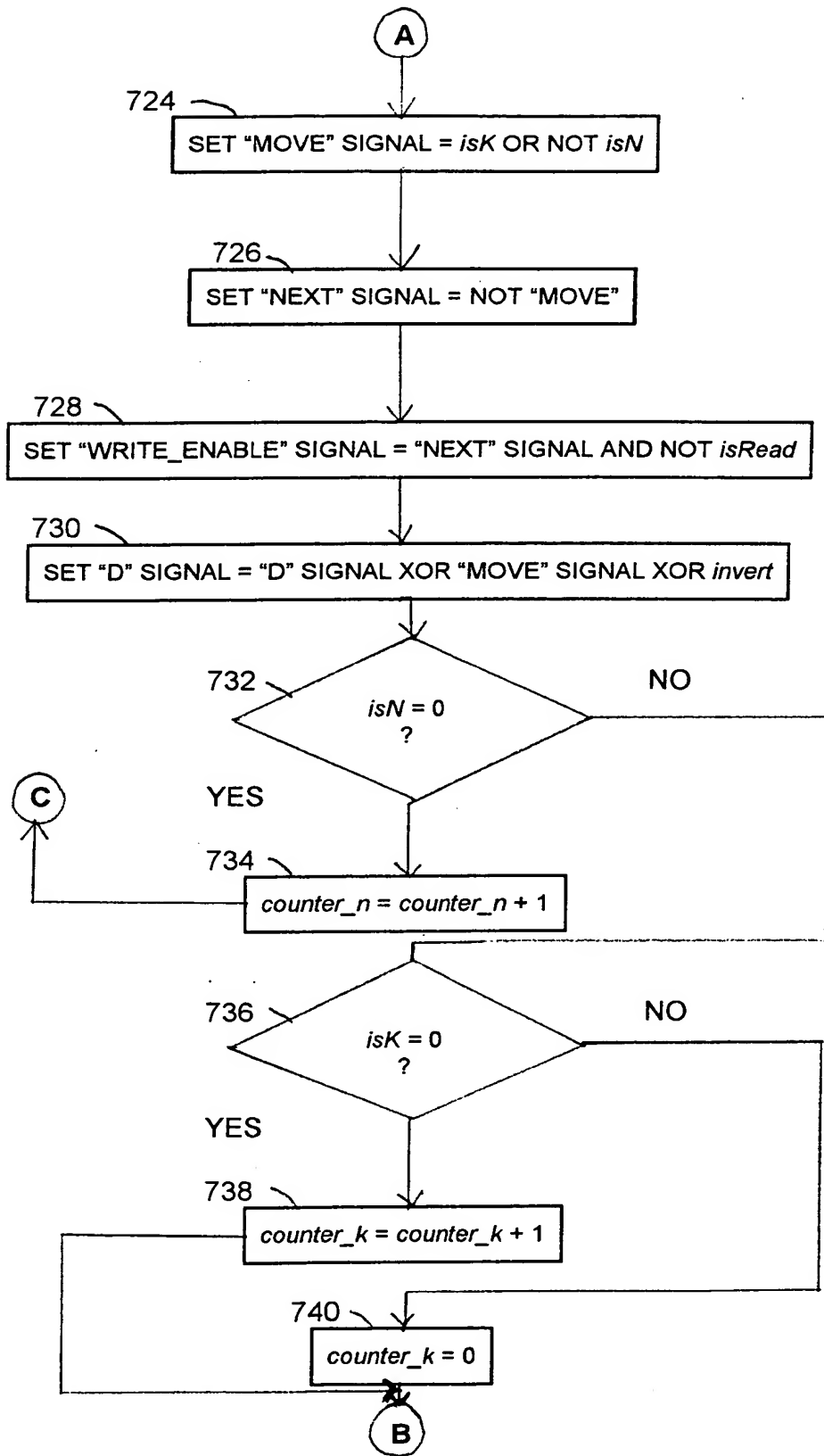


FIG. 7B



10027311-122101

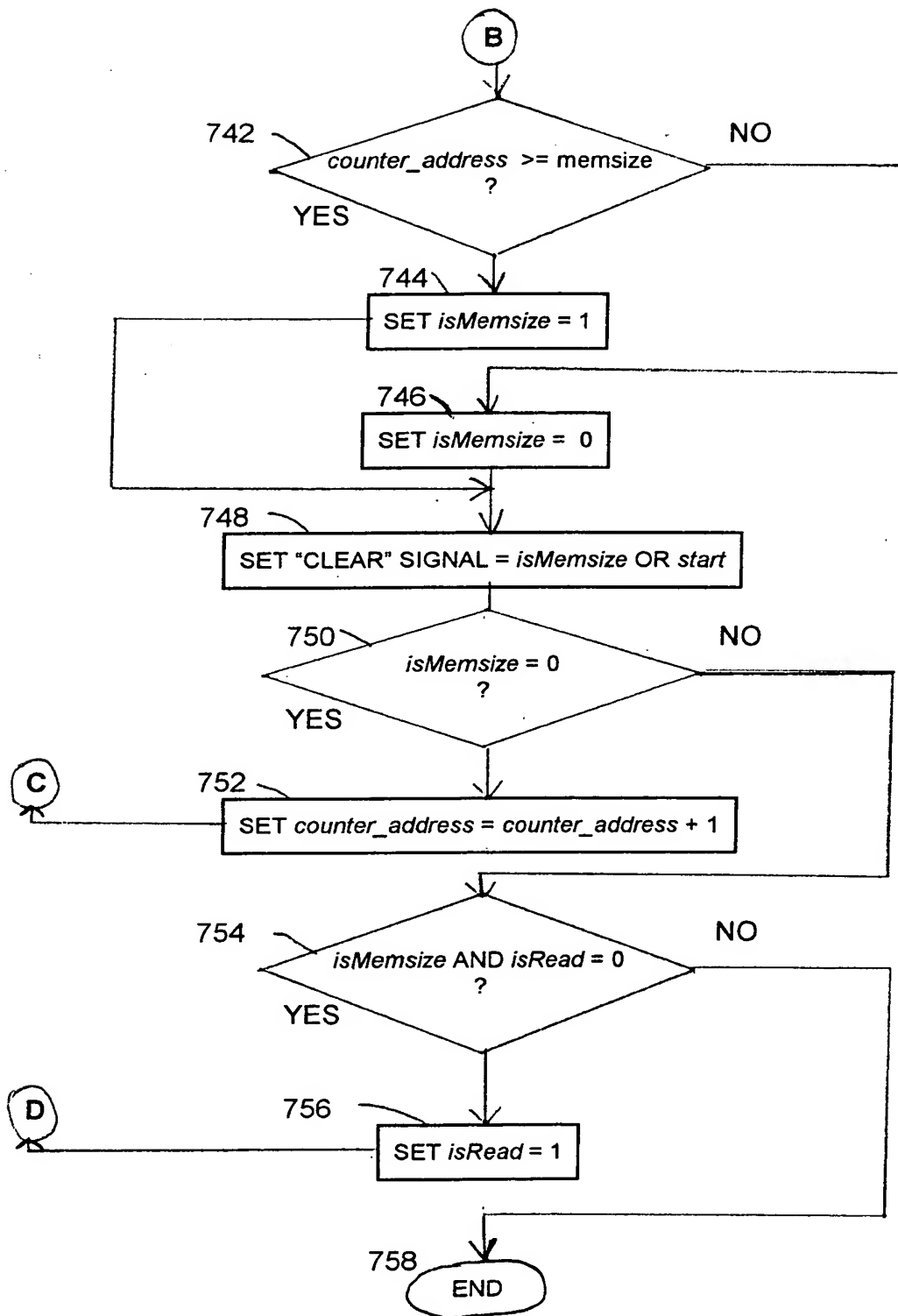


FIG. 7C